HIMACHAL PRADESH UNIVERSITY
MASTER OF TECHNOLOGY in COMPUTER SCIENCE
M. Tech. (Computer Science)
Effective from 2013

About the Course
The M. Tech. (Computer Science) is a two years full time post graduation course spread over four semesters. In this course, the students will be taught, the core courses of Computer Science.

Eligibility
Candidates, who have passed Master degree with minimum of 55% marks (50% marks for SC/ST category), or equivalent Grade point, in Mathematics/Physics/Electronics/Computer Application (MCA)/Computer Science/Information Technology/4-years Bachelor’s degree in Engineering/Technology.

OR

Any examination, of university in foreign country, recognized as equivalent for the above purpose by equivalence committee of its own or on recommendation of Association of Indian Universities with 55% marks (50% marks for SC/ST).

Mode of Selection
The admission to M. Tech. (Computer Science) will be through the merit of an entrance test of duration 3 hours to be conducted by H.P. University, Shimla. The entrance test shall be of 200 MCQ type questions of one mark each, comprising of the following components:

<table>
<thead>
<tr>
<th>Section</th>
<th>Contents</th>
<th>Marks</th>
</tr>
</thead>
<tbody>
<tr>
<td>A</td>
<td>Logical Ability</td>
<td>40</td>
</tr>
<tr>
<td>B</td>
<td>Mathematics of Graduation level</td>
<td>40</td>
</tr>
<tr>
<td>C</td>
<td>English and General Awareness</td>
<td>20</td>
</tr>
<tr>
<td>D</td>
<td>Computer Ability</td>
<td>100</td>
</tr>
<tr>
<td><strong>Total</strong></td>
<td></td>
<td><strong>200</strong></td>
</tr>
</tbody>
</table>

The Candidates who have qualified GATE/NET/SLET in Computer Science or Engineering will be given 20% extra of the marks obtained in the entrance test for calculating the merit for the purpose of admission.

Minimum pass marks in the entrance test shall be 35% i.e. 70 marks out of a total of 200.

Number of Seats
Total number of seats 18 + 5 seats for in-service candidates

In case, any of these seats remain vacant due to the unavailability of suitable eligible candidates, seats for in-service candidates shall remain vacant and under no circumstances, conversions shall be allowed.

Being a self financing course, in case the numbers of selected candidates after entrance exam are less than nine (09), then the course will not be run for that academic year.
Reservation for M. Tech.:  
As per the H.P. University rules 75% seats will be filled out of the candidates who have passed their qualifying examination from Himachal Pradesh University and remaining 25% seats will be filled on all India basis. The other reservation rules of H.P. University shall be applicable as per the roster followed for M.C.A. admission.

In-service Candidates  
The ‘in-service’ candidate shall be a candidate who is an employee (on a regular or contract basis) of Govt. of Himachal Pradesh (including Colleges in & Universities under the Govt.) or Govt. of India or Boards/Corporations under state or centre governments having their office(s) in the state and is serving the organization in which currently employed for at least last 2 years. An in-service candidate has to submit the following along with the application form:

a) The appointment letter from the employer and a certificate from the employer that he/she has been working with the concerned organization for at least last two years and shall continue to be on their roll for at least whole of the duration of the course,

b) NOC on the prescribed proforma provided in the prospectus from his employer that in case the candidate is selected for the admission, he/she shall be granted a study leave for a period of two years from the date of the commencement of the course as mentioned in the prospectus for the course.

Upon confirmation of the admission, a letter on the prescribed proforma provided in the prospectus from the employer sanctioning study leave for a period of 2 years is to be submitted in the department on or before the stipulated last date of admission along with an affidavit from the court duly signed by the notary clearly stating that if the information supplied in any of the above mentioned documents is found to be contrary to the facts at any stage, the candidate shall be held responsible for it and the university shall have the right to cancel the admission/degree of the candidate.

The admission of such candidates shall be confirmed only if a letter from the employer sanctioning the study leave of two years is deposited within the stipulated period for the admission as mentioned in the prospectus. The validity of the above mentioned documents shall be assessed by the Department and the Department shall also have the right of corresponding with the employer of the candidate in case of any doubts regarding these documents. The decision of the Department in rejecting or accepting these documents shall be final. In any case, notwithstanding the admission to the said course, if the department finds that the candidate has supplied false or misleading information through any of the above mentioned documents, the university shall have the right to a) cancel the admission/degree of the candidate, and b) to initiate civil/criminal proceeding against the candidate.

Fee Structure  
a) From all students, except for the in-service candidates, a fee of Rs.30,000/- (Rupees thirty thousand only) p.a. and an Equipment Fee of Rs.5,000/- (Rupees five thousand only) p.a. along with other university dues as mentioned in the prospectus shall be charged by the department. Two demand drafts of Rs.30,000/- and Rs.5,000/- payable at Shimla in favour of the
Chairman, Department of Computer Science, HPU, Shimla-5 are to be deposited at the time of admission and subsequently in the beginning of the 2nd year.

b) For in-service students, the fee shall be Rs.50,000/- (Rupees fifty thousand only) p.a. and an Equipment Fee of Rs.5,000/- (Rupees five thousand only) p.a. along with other university dues as mentioned in the prospectus shall be charged by the department. Two demand drafts of Rs.50,000/- and Rs.5,000/- payable at Shimla in favour of the Chairman, Department of Computer Science, HPU, Shimla-5 are to be deposited at the time of admission and subsequently in the beginning of the 2nd year.

c) The other university dues as mentioned in the prospectus shall be deposited at the time of admission and in the beginning of the 2nd year on the main cash counter of H.P. University, Shimla.

d) All the students shall have to pay an amount of Rs.2,500/- (Rupees two thousand five hundred only) as the dissertation thesis submission fee in the beginning of fourth semester in the form of a demand draft in favour of the Chairman, Department of Computer Science, H.P. University, Shimla.

Other Conditions

English will be the medium of instruction and examination.

Scheme of the Examination:

1. Each theory paper shall be of 3 hours duration and shall carry 100 marks (75 marks for end semester theory examination and 25 marks for continual internal assessment).

2. In each theory paper, nine questions are to be set. Two questions are to set from each Unit and candidate is required to attempt one question from each unit. Question number nine will be compulsory, which will be of short answer type with 5-10 parts, out of the entire syllabus. In all, five questions are to be attempted.

3. Each practical paper shall be of 3 hours duration and will carry 100 marks (50 marks for end semester practical examination and 50 marks for continual internal assessment).

4. In the III semester each student shall be:
   a) Attached to a teacher of the Department who shall act as a guide for seminar, minor project and Dissertation work of the students in III & IV semesters.
   b) Assigned a topic by the guide and the students are required to prepare and present a seminar.
   c) Assigned a topic for carrying out a minor project which normally may be related to the findings of a thorough study on a research area of significance which is relevant to the field of Computer Science. At the end of III semester, each student has to submit 4 nos. of copies of spiral bound report of his findings on the topic allotted to him/her in the office of the Department along with the softcopy of the same on a CDROM/DVD.
   d) Assessed and evaluated for minor project by an external examiner.

5. In the IV semester, each student is required to:
   a) Work on a topic in a significant area of research relevant to the field of Computer Science for dissertation thesis under the guidance of his/her guide.
   b) Submit 5 nos. of hard bound copies of the dissertation work in the office of the Department along with the softcopy of the same on a CDROM/DVD.
   c) Ensure the quality of the research work carried out by them in the III & IV semester, each student, hence is required to participate in some relevant regional/national level conferences/ workshops/ seminars.
during these semesters. Therefore, it is mandatory for each student to publish at least one research paper (Full paper or abstract or extended abstract in the proceedings in print version/ e-version) in a regional/national level conference/ workshop/ Seminar. The certificate/proof of the published paper along with the paper shall form the part of the submitted dissertation work. The students shall not be allowed to submit their dissertation work without completing this requirement.

6. A copy of the Dissertation work of each candidate shall be sent to the external examiner well in advance by the examination branch of HPU before the conduct of the viva-voce examination.
7. The dissertation will be jointly evaluated by internal guide and external examiner.
8. Internal assessment will be given on the basis of class tests (best of 2 in a semester), seminars, surprise quizzes, class participation and regularity of the student in the class.

**Honourarium**

1. Since the M. Tech. course is being run in the self financing mode, all the teachers shall be paid an honourarium on lecture basis as per university norms for all the lectures taken for engaging theory as well as practical classes in the M. Tech. course in the department.
2. The external examiner shall be paid an honourarium of Rs.200/- (Rupees two hundred only) per student for evaluating minor project in the III semester subjected to an evaluation of a maximum of 10 students in a single examination session by an examiner.
3. The experts invited for conducting the viva-voce examination for evaluating the M. Tech. students in the IV semester shall be paid an honourarium of Rs.500/- (Rupees five hundred only) per thesis for its evaluation and Rs.200/- (Rupees two hundred only) per student for conducting viva-voce per dissertation thesis subjected to a maximum of 5 thesis in a single examination session by an expert.
4. All the teachers guiding the students in M. Tech. IV semester dissertation work shall be paid on honourarium of Rs.1,000/- (Rupees one thousand only) per student.
5. All the expenditure incurred with respect to the honourarium etc. shall be met out of the funds generated through the running of M. Tech. course.

**Promotion Rule**
The promotion norms in the MCA course are to be followed for the M. Tech. course too.
## MASTER OF TECHNOLOGY in COMPUTER SCIENCE
### M. Tech. (Computer Science)

### First Semester

<table>
<thead>
<tr>
<th>Paper No.</th>
<th>Title</th>
<th>Periods per Week</th>
<th>Max Marks (Theory)</th>
<th>Continual Internal Assessment</th>
<th>Exam Duration Hours</th>
</tr>
</thead>
<tbody>
<tr>
<td>MT-101</td>
<td>Computer Architecture and Parallel Processing</td>
<td>4</td>
<td>75</td>
<td>25</td>
<td>3</td>
</tr>
<tr>
<td>MT-102</td>
<td>Software Engineering</td>
<td>4</td>
<td>75</td>
<td>25</td>
<td>3</td>
</tr>
<tr>
<td>MT-103</td>
<td>Computer Oriented Optimization Method</td>
<td>4</td>
<td>75</td>
<td>25</td>
<td>3</td>
</tr>
<tr>
<td>MT-104</td>
<td>Data Structure &amp; Algorithm Analysis in C</td>
<td>4</td>
<td>75</td>
<td>25</td>
<td>3</td>
</tr>
<tr>
<td>MT-105</td>
<td>Operating System and Case Study</td>
<td>4</td>
<td>75</td>
<td>25</td>
<td>3</td>
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<tr>
<td>MT-106</td>
<td>Practical on MT-103</td>
<td>6</td>
<td>50</td>
<td>50</td>
<td>3</td>
</tr>
<tr>
<td>MT-107</td>
<td>Practical on MT-104</td>
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<td>50</td>
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### Second Semester

<table>
<thead>
<tr>
<th>Paper No.</th>
<th>Title</th>
<th>Periods per Week</th>
<th>Max Marks (Theory)</th>
<th>Continual Internal Assessment</th>
<th>Exam Duration Hours</th>
</tr>
</thead>
<tbody>
<tr>
<td>MT-201</td>
<td>Object Oriented Programming with JAVA</td>
<td>4</td>
<td>75</td>
<td>25</td>
<td>3</td>
</tr>
<tr>
<td>MT-202</td>
<td>Computer Networks</td>
<td>4</td>
<td>75</td>
<td>25</td>
<td>3</td>
</tr>
<tr>
<td>MT-203</td>
<td>Distributed Data Base Management System</td>
<td>4</td>
<td>75</td>
<td>25</td>
<td>3</td>
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<tr>
<td>MT-204</td>
<td>Data Warehousing and Data Mining</td>
<td>4</td>
<td>75</td>
<td>25</td>
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<tr>
<td>MT-205</td>
<td>Software Quality and Testing</td>
<td>4</td>
<td>75</td>
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<td>MT-206</td>
<td>Practical on MT-201</td>
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<td>3</td>
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<tr>
<td>MT-207</td>
<td>Practical on MT-203</td>
<td>6</td>
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### Third Semester

<table>
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<tr>
<th>Paper No.</th>
<th>Title</th>
<th>Periods per Week</th>
<th>Max Marks (Theory)</th>
<th>Continual Internal Assessment</th>
<th>Exam Duration Hours</th>
</tr>
</thead>
<tbody>
<tr>
<td>MT-301</td>
<td>Research Methodology in Computer Science</td>
<td>4</td>
<td>75</td>
<td>25</td>
<td>3</td>
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<tr>
<td>MT-302</td>
<td>Elective – 1</td>
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<td>75</td>
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<table>
<thead>
<tr>
<th>Paper No.</th>
<th>Title</th>
<th>Periods per Week</th>
<th>Max Marks</th>
<th>Continual Internal Assessment</th>
<th>Evaluation Marks</th>
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<tbody>
<tr>
<td>MT-303</td>
<td>Seminar</td>
<td>8 in computer lab 4 in library</td>
<td>100</td>
<td>25</td>
<td>75 ( by Department )</td>
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<td>MT-304</td>
<td>Minor Project</td>
<td>8 in computer lab 4 in library</td>
<td>200</td>
<td>100</td>
<td>100 ( by External Examiner )</td>
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</tbody>
</table>

### Fourth Semester

<table>
<thead>
<tr>
<th>Paper No.</th>
<th>Title</th>
<th>Periods per Day</th>
<th>Maximum Marks</th>
<th>Continual Internal Assessment</th>
<th>Publications</th>
<th>Viva-Voce Examination</th>
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<tr>
<td>MT-401</td>
<td>Dissertation</td>
<td>5</td>
<td>500</td>
<td>125</td>
<td>125</td>
<td>250</td>
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</tbody>
</table>
## List of Electives

<table>
<thead>
<tr>
<th>Code</th>
<th>Course</th>
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</thead>
<tbody>
<tr>
<td>MT-E01</td>
<td>Advanced Software Engineering Concepts</td>
</tr>
<tr>
<td>MT-E02</td>
<td>Artificial Intelligence &amp; Expert System</td>
</tr>
<tr>
<td>MT-E03</td>
<td>Automata Theory and Compiler Design</td>
</tr>
<tr>
<td>MT-E04</td>
<td>Cyber Law</td>
</tr>
<tr>
<td>MT-E05</td>
<td>Cloud Computing</td>
</tr>
<tr>
<td>MT-E06</td>
<td>Distributed Systems</td>
</tr>
<tr>
<td>MT-E07</td>
<td>Graph Theory</td>
</tr>
<tr>
<td>MT-E08</td>
<td>Research Methodology</td>
</tr>
<tr>
<td>MT-E09</td>
<td>Service Oriented Architecture</td>
</tr>
</tbody>
</table>
MT-101  Computer Architecture & Parallel Processing  L  T  3  1

UNIT – I
RTL, Bus and memory transfer, Arithmetic microoperations, Logic microoperations, Shift microoperations, Arithmetic Logic Shift unit
Instruction codes, Computer registers and instructions, Timing and control, Instruction cycle, MRIs, I/O and Interrupts, Complete computer description, Design of basic computer, Design of Accumulator logic

UNIT – II
Control memory, Address sequencing, Computer configuration, Microinstruction format, Symbolic microinstructions, Design of control unit
Introduction to CPU, General Register and stack organization, Instruction formats, Addressing modes, Data transfer and manipulation, RISC, CISC
Parallel Computer Models: The state of computing, Multiprocessors and multicomputers, Multivector and SIMD Computers, PRAM and VLSI models

UNIT – III
Program and Network Properties: Conditions of Parallelism, Program partitioning and scheduling, Program flow mechanisms, System interconnect architectures
Principles of Scalable Performance: Performance metrics and measures, Parallel processing applications, Speedup Performance laws, scalability analysis and approaches.
Processor and Memory Hierarchy: Advanced processor technology, Superscalar and vector processors

UNIT – IV
Memory hierarchy technology, Virtual memory technology, Bus, Cache and Shared Memory: Backplane bus systems, cache memory organizations, Shared memory Organizations, Sequential and weak consistency models
Pipelining and Superscalar Techniques: Linear pipeline processors, nonlinear pipeline processors, Instruction Pipeline design, Superscalar and superpipeline design
Multiprocessors and Multicomputers: Multiprocessor system interconnects, Cache coherence and synchronization mechanisms, Three generations of multicomputers, Message passing mechanisms

Text Books:

Reference Book:

Note: In each theory paper, nine questions are to be set. Two questions are to be set from each Unit and candidate is required to attempt one question from each unit. Question number nine will be compulsory, which will be of short answer type with 5-10 parts, out of the entire syllabus. In all, five questions are to be attempted.
UNIT – I

UNIT – II

UNIT – III

UNIT – IV

Text Books:

Reference Books:

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answer type with 5-10 parts, out of the entire syllabus. In all, five questions are to be attempted.
MT-103  Computer Oriented Optimization Methods

UNIT – I

UNIT – II

UNIT – III

UNIT – IV
Game theory: Significance, essential features and limitations; Maximax and minimax principle, Game with pure & mined strategies, sul-game method (case of 2xn or mx2 methods), Probability method, graphic method, algebraic method.
Inventory Control: Introduction, Inventory Control, Selective Control Techniques, ABC Analysis Procedure, Economics Lot Size Problems, Problem of EQQ With shortage, Inventory Control Techniques Uncertain Demand, Stochastic Problems.

Text Book:

Reference Books:

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UNIT – I
Preliminaries: Concept & notation, common operation on data structures, algorithm complexity, time-space trade off between algorithm, physical & logical representation of different data structures.
Arrays: Arrays defined, representing arrays in memory, Various operation (traversal, insertion, deletion), Multidimensional arrays, Sequential allocation, Address calculation, Sparse arrays.
Stack: Stack Model, Implementation of Stacks, Applications of Stacks.

UNIT – II
Queue: Queue Model, Array Implementation of Queues, Applications of Queues.
Hashing: Definition, Hash Fuction, Separate Chaining, Open Addressing-Linear Probing, Quadratic Probing, Double Hashing, Rehashing, Extendible Hashing.

UNIT – III

UNIT – IV
Graphs: Definitions, Representation Of Graphs, Topological Sort, ShortestPath Algorithms- Unweighted Shortest Paths, Dijkstra’s Algorithm, Graph With Negative Edge Costs, Acyclic Graphs, All- Pairs Shortest, Minimal Spanning Tree- Prim’s Algorithm, Kruskal’s Algorithm, Application Of DepthFirst Search- Undirected Graphs, Biconnectivity, Euler Circuits, Directed Graphs.
Algorithm Design Techniques: Greedy Algorithms- A Simple Scheduling Problem, Huffman Codes, Divide And Conquer- Running Time Of Divide and Conquer Algorithms, Closets-Points Problem, The Selection Problem,
Dynamic Programming- Using A Table Instead Of Recursion, Ordering Matrix Multiplications, Optimal Binary Search Tree, All-Pairs Shortest Path, Backtracking Algorithms- the Turnpike Reconstruction Problem.

Text Books:
1. Mullis Cooper: Spirit of C: Jacob Publications
2. Yashwant Kanetkar: Let us C: BPB
5. Robert L. Kruse: Data Structures & Program Design: PHI.

Note: In each theory paper, nine questions are to be set. Two questions are to set from each Unit and candidate is required to attempt one question from each unit. Question number nine will be compulsory, which will be of short answer type with 5-10 parts, out of the entire syllabus. In all, five questions are to be attempted.
UNIT – I

UNIT – II
Process Synchronization: The Critical Section Problem, Synchronization Hardware, Semaphores, Classical Problems of Synchronization, Critical Regions.
Deadlocks: Deadlock Characterization, Methods For Handling Deadlocks, Deadlock Prevention, Deadlock Avoidance, Deadlock Detection, Recovery From Deadlock.
Memory Management: Logical & physical address space, Swapping, Continuous Allocation (single partition, multiple partition), internal, external fragmentation, Paging, Segmentation, Segmentation With Paging, Virtual Memory, Demand Paging, Performance Of Demand Paging, Page Replacement, Page Replacement Algorithms— FIFO, optimal, LRU, LRU approximation algorithms, counting algorithms Thrashing, Demand Segmentation.

UNIT – III
Secondary Storage Structure: Disk Structure, Disk Scheduling, FCFS, SSTF, SCAN, C-SCAN, Look Scheduling, Selection of A Scheduling Algorithm, Disk Management—disk formatting, boot block, bad blocks.

UNIT – IV
Case Study: UNIX system: Design principles, Programmer interface (File manipulation, Process control, Signals, Process groups, Information Manipulation), Process management (Process control block, CPU scheduling), Memory management (Swapping, Paging), file system (Blocks & fragments, Inodes, Directories), I/O/ system (Block buffer cache, Raw device interface, C-lists).
Case study: Windows NT: Design principles, System components (H/w abstraction layer, Kernel, Executive), File system (Internal layout, Recovery, Security, Volume management & fault tolerance, Compression), Networking (Protocols, Distributed-processing mechanism, Domains), Programmer interface (Access to kernel objects, Process management, Inter-process communication, Memory management).

Case Study: MS-DOS: User’s view of MS-DOS, System’s view of MS-DOS, Programmer's view of MS-DOS system calls.

**Text Book:**

**Reference Books:**

**Note:** In each theory paper, nine questions are to be set. Two questions are to set from each Unit and candidate is required to attempt one question from each unit. Question number nine will be compulsory, which will be of short answer type with 5-10 parts, out of the entire syllabus. In all, five questions are to be attempted.
UNIT – I
Introduction To Object Oriented Programming: Data Abstraction, Encapsulation, Inheritance (Public, Protected And Private), Polymorphism, Information Hiding.
Java Elements: Data Types, Literal and Variables, Operators–Arithmetic, Bitwise, Relational, Boolean Logical, Assignment, The ‘?’ Operator, Operator Precedence, Control Statements–Selection (if, switch), Iteration Statements (while, do-while, for) Jump Statements (break, continue, return), Arrays (One-dimensional, Multi-Dimensional).

UNIT – II
Introducing Classes: Class Fundamentals, Declaring Objects, Methods, Constructors, This’ Keyword, Over loading Methods.
Inheritance: Inheritance Basics, Protected Members, Method Overriding, Multiple Inheritance, Template Classes and Functions.
Exception Handling: Fundamental, Exception Types, Uncaught Exceptions, Try And Catch, Dealing With Exceptions (try, throw, throws, finally).

UNIT – III

UNIT – IV

Text Book:

Reference Books:
**Note:** In each theory paper, nine questions are to be set. Two questions are to set from each Unit and candidate is required to attempt one question from each unit. Question number nine will be compulsory, which will be of short answer type with 5-10 parts, out of the entire syllabus. In all, five questions are to be attempted.
UNIT – I
Data Communication, Network Components, Protocol & Standards,
Standard Organization, Topologies, Transmission modes, Categories of
Networks, Uses, Applications. The OSI Reference Model: Layered architecture,
Functions of layers, TCP/IP reference model, Comparison of OSI & TCP/IP
models. Internet, frame relay, ATM, Ethernet, Wireless LAN. Physical layer:
Theoretical basis for data communications-Fourier analysis, bandwidth
limited signals, maximum data rate of a channel, Guided and wireless
transmission media, Communication satellites, Public switched telephone
networks, mobile telephone system, Cable television.

UNIT – II
Data Link and Mac Layer: Design issues, Framing techniques, Flow control,
Error Control, Error Detecting code and Error Correcting codes, Data link
Control and Protocols-- For noiseless Channel – Simplest Protocol, Stop-
andWait Protocol, For Noisy Channel-- Stop-and-Wait ARQ, Go-Back-N ARQ,
and Selective-Repeat ARQ Protocol, HDLC Protocol, and PPP Protocol, Multiple
Access-- Random Access-- MA, CSMA, CSMA/CD, CSMA/CA,
Controlled Access—Reservation, Polling, Token passing, Channelization--
FDMA, TDMA, CDMA, and IEEE standards-- 802.3 (Ethernet), 802.4 (Token
Bus), 802.5 (Token Ring), 802.11(Wireless LAN), 802.15 (Bluetooth).

UNIT – III
Network and transport Layer: Network layer design issues, Addressing,
Routing algorithms-shortest path routing, flooding, distance vector routing,
link state routing, hierarchical routing, broadcast routing, multicast routing,
routing for mobile hosts, Congestion Control algorithms – congestion
prevention policies, congestion control in virtual circuit & datagram
subnetworks, definition of quality of service, Internetworking – Tunneling,
internet-work routing, fragmentation, Network layer in Internet –IP protocol,
IP Address, OSPF, BGP, Internet multicasting, Mobile IP, Ipv6,Transport
Layer: Concept of transport service, elements of transport protocols, A simple
transport protocol, Remote procedure call, Performance issues in computer
networks.

UNIT – IV
Application layer services protocols & Network Security: DNS, SMTP, FTP,
TELNET, HTTP,WWW, Attacks on Computers & Computer security-- Need for
security, approaches, principles, types of attacks, Cryptography concept and
techniques, Symmetric Key algorithms-- (DES), Asymmetric key algorithms--
RSA, Digital signature , Firewalls. Internet radio, VoIP, E-mail security, Web
security, social issues in network security,

Reference Books:
   Mcgraw Hill.
   Publishing Co.
5. Fred Halsall, “Data Communications, Computer Networks”, Pearson Education.

**Note:** In each theory paper, nine questions are to be set. Two questions are to set from each Unit and candidate is required to attempt one question from each unit. Question number nine will be compulsory, which will be of short answer type with 5-10 parts, out of the entire syllabus. In all, five questions are to be attempted.
UNIT – I

UNIT – II

UNIT – III
Query Processing: Problem, objectives, Complexity of Relational Algebra operations, Characterization of query processing (Language, Types of Optimization, Optimization timing, Statistics, Decision sites, Exploitation of network topology &Replicated fragments, Use of semijoins), Layers of Query processing (Query decomposition, Data localization, Global & Local query optimizations).
Distributed Concurrency Control: Serializability theory, Taxonomy of concurrency control mechanism, Locking based concurrency control algorithm (centralized 2pl, primary copy 2pl, distributed 2pl), Timestamp based concurrency control algorithm (conservative & multiversion TO algorithm), Optimistic concurrency control algorithm, Deadlock management, prevention, avoidance, detection & resolution.

UNIT – IV
Distributed DBMS Reliability: Reliability concepts & measures (system, state & failures, reliability & availability, mean time between failures/repair), Failures & fault tolerance in distributed system (reason for failures, fault tolerance approaches & techniques), Failures in Distributed DBMS (transaction, system, media & communication failure), Local reliability protocols (architectural considerations, recovery, information execution of
LRM commands, checkpointing, handling media failure), Distributed Reliability Protocols (Components, Two-Phase commit protocol, Variation of 2PC).

**Text Books:**

**Reference Books:**

**Note:** In each theory paper, nine questions are to be set. Two questions are to set from each Unit and candidate is required to attempt one question from each unit. Question number nine will be compulsory, which will be of short answer type with 5-10 parts, out of the entire syllabus. In all, five questions are to be attempted.
UNIT – I
Introduction: DSS, Data warehouse Architecture, Data Staging & ETL, Multidimensional Model, Meta data, Accessing data warehouse, ROLAP, MOLAP, HOLAP
System Lifecycle: Risk factors, Top-down, Bottom-up, Data mart design phases, Methodological framework, Testing data marts
Data Sources: Inspecting and normalizing schemata, Integration problems, Integration phases, Mapping
User Requirements & Conceptual Modeling: Glossary based requirements analysis, Goal-oriented requirements analysis, Dimensional Fact Model, Advanced modeling, Events and Aggregation, Time, Formalizing the dimensional fact model
Conceptual Design: ER schema based design, Relational schema based design, XML schema based design, Mixed approach design

UNIT – II
Logical Modeling & Design: MOLAP, HOLAP & ROLAP systems, Views, Temporal scenarios, Fact schemata to star schemata, View materialization, View Fragmentation, Populating - reconciled databases, dimension tables, fact tables & materialized views, Cleansing data
Data Warehouse Components: Overall architecture, database, Sourcing, acquisition, cleanup and transformation tools, Metadata, Access tools, Administration and management, Info delivery System
Building a Data Warehouse: Considerations - business, design, technical & implementation, Integrated solutions, Benefits

UNIT – III
Mapping Data Warehouse to a Multiprocessor Architecture: Relational database technology, Database architectures for parallel processing, Parallel RDBMS features and vendors
DBMS Schemas & Decision Support: Data layout for best access, Multidimensional data models, Star schema
Data Tools and Metadata: Tool requirements, Vendor approaches, Access to legacy data, Transformation engines, Metadata - definition, interchange initiative, repository, trends, Reporting & Query Tools – categories
OLAP: Need, Multidimensional data model, guidelines, Multidimensional Vs multirelational OLAP, Categorization of OLAP tools

UNIT – IV
Introduction: Data mining, Measuring effectiveness, Discovery Vs prediction, Overfitting, Comparing the technologies, Decision trees, Where to use them, General idea, How do they work, Strengths and weaknesses
Techniques and Algorithms: Neural networks - uses, making predictions, different kinds, Kohonen feature map, their working, Nearest Neighbour & Clustering – uses, predictions and differences, their working, Genetic Algorithms – uses, cost minimization, cooperative strategies, their working, Rule Induction – uses, evaluation of rules, rules Vs decision trees, their working, Using the right technique, Data mining & business process
Text Books:

Reference Books:

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UNIT – I
Assuring Software Quality Assurance (SQA): Objectives, goals, responsibilities, life cycle, SQA planning, SQA monitoring and controlling, testing, setting standards and procedures, Developing and controlling relevant metrics, SQA activities- revision, process evaluation, software standards.

UNIT – II
Software Quality Meterics: Objectives, Software metrics, Software Quality metrics framework, Software Quality metrics features, Development of software quality metrics- SATC’s approach, Kitchenham’s approach, Abreu’s approach, Victor’s approach, Selection of Software Quality metrics- Size related metrics, complexity metrics, Halstead metrics, quality metrics.

UNIT – III
Functional Testing: Boundary Value Analysis- Introduction & Definition, Generalising, limitations, Robustness testing, Worst case testing, Test cases.
Equivalence Class Testing - Introduction & Definition, Weak normal, strong normal, Weak robust, Strong robust, Test cases.
Decision Table Based Testing- Introduction & Definition, technique, test cases.

UNIT – IV
Structural Testing: Path testing - Introduction & definition, DD-path, Test coverage metrics, McCabe’s basis path method, its observations and complexity.
Data Flow Testing: Definition, data flow graphs, data flow model, Data flow testing strategies.
Levels of Testing: Traditional view of testing levels, Integration Testing (Decomposition based integration), Unit Testing, System Testing.

Text Books:
Reference Books:

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MT-301 Research Methodology in Computer Science

Lectures 4 per week

UNIT-I

Research Aptitude: Meaning of Research, Objectives of Research, Motivation in Research, Types of Research, Research Approaches, Research Methods versus Methodology, Research and Scientific Method, Importance of Knowing How Research is done.

Research Process: Reviewing the literature, Formulation of research problem, Nature and type of variables, Hypothesis - meaning, types, development of hypothesis and its testing, Meaning & Functions of Research Design

UNIT-II

Data Analysis: Sources, acquisition and interpretation of data, Quantitative and qualitative data, Graphical representation and mapping of data, Sensitivity Analysis with Data Tables, Optimization with EXCEL Solver, Summarizing Data with Histograms and Descriptive Statistics, Pivot Tables, Summarizing Data with database statistical functions, using correlation, Multiple Regression, Using Sampling to Analyze Data

UNIT-III

Significance of Report Writing: Different Steps in writing Report, Layout of the Research Report, Types of Reports, Mechanics of Writing a Research Report, Art of scientific writing- Steps to better writing, flow method, organization of material and style, Drawing figures, graphs, tables, footnotes, references etc. in a research paper

UNIT-IV

Use of internet in research work: Use of internet networks in research activities in searching material, paper downloading, submission of papers, relevant websites for journals and related research work. Introduction to Patent laws etc., process of patenting a research finding, Copy right, Cyber laws.

References:

1. Research Methodology Methods and Techniques, Kothari, C. R., Wiley Eastern
Note: In each theory paper, nine questions are to be set. Two questions are to be set from each Unit and candidate is required to attempt at least one question from each unit. Question number nine will be compulsory, which will be of short answer type with 5-10 parts, out of the entire syllabus. In all, five questions are to be attempted.

Electives

MT-E01 Advanced Software Engineering Concepts L T
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UNIT – I

UNIT – II
Object Methodology & Requirement Elicitation: Introduction to Object Oriented Methodology, Overview of Requirements Elicitation, Requirements Model-Action & Use cases, Requirements Elicitation Activities, Managing Requirements Elicitation

UNIT – III

UNIT – IV

Text Book:

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answer type with 5-10 parts, out of the entire syllabus. In all, five questions are to be attempted.

**MCA – E02  Artificial Intelligence and Expert Systems**  

UNIT – I  
Overview Of A.I.: Definition Of AI, The Importance Of AI, Previous Works In The History Of AI, AI And Related Fields, Problems, Problem Spaces And Search. Knowledge: General Concepts –Definition and Importance of Knowledge, Knowledge-Based Systems, Representation Of Knowledge, Knowledge Organization, Knowledge Manipulation, Acquisition Of Knowledge.

UNIT – II  

UNIT – III  
Natural Language Processing: Overview of Linguistics, Grammer and Languages, Syntactic Processing, Semantic Analysis, Morphological, Discourse and Pragmatic Processing, Natural Language Generation, Natural Language Systems.

UNIT – IV  

**Text Book:**  

**Reference Books:**  

**Note:** In each theory paper, nine questions are to be set. Two questions are to set from each Unit and candidate is required to attempt one question from each unit. Question number nine will be compulsory, which will be of short answer type with 5-10 parts, out of the entire syllabus. In all, five questions are to be attempted.

**MT – E03  Automata Theory and Compiler Design**  

UNIT – I  
Finite Automata and Regular Expression: Finite State System, Basic Definition, Deterministic and Non-Deterministic Finite Automata (Only Definition), Finite Automata With Output, Regular Expression.
Turing Machines: Definition Of Various Version Of Touring Machines, Deterministic, Non-Deterministic, Two-Way, Infinite Tape, Multi Tape, Multi Head, Statements Of Their Equivalence (Without Proof), Construction Of Turing Machines (Any Model) For Log N; N!, N^2;

UNIT – II
Properties Of Context -Free Languages : The Pumping Leema For CFL’S Closure Properties Of CFL’S , Decision Algorithms For CFL’S.

UNIT – III
Syntax Analysis, The Role Of Parser, Context Free Grammars, Writing A Grammer, Top-Down Parsing (Recursive-Descent Parsing, Predictive Parsing, Transition Diagram For Predictive Parsing,

UNIT – IV
Non Recursive Predictive Parsing, First And Follow, Ll(1) Grammers, Error Recovery In Predictive, Parsing .
Bottom-Up Parsing: Handles, Handle Pruning, Stack Implementation In Shift Reduce Parsing, Conflicts In Shift Reducing Parsing, LR-Parsers, LR Algorithm, LR Grammars, Constructing SLR Parsing Tables, Using Ambiguous Grammars, Error Recovery In LR Parsing.

Text Book:

Reference Books:

Note: In each theory paper, nine questions are to be set. Two questions are to set from each Unit and candidate is required to attempt one question from each unit. Question number nine will be compulsory, which will be of short answer type with 5-10 parts, out of the entire syllabus. In all, five questions are to be attempted.

MT – E04 Cyber Law

UNIT – I

UNIT – II

UNIT – III
The Information Technology Act, 2000:
Introduction: Definition, A Brief Summary of the Act. Digital Signature & Electronic Governance (Sections 3 to 10). Secure Electronic Records & Secure Digital Signatures (Sections 14 to 16).

UNIT – IV
Regulation of Certifying Authorities (Sections 17 to 34). Digital Signature Certificates (Sections 35 to 39). Duties of Subscribers (Sections 40 to 42). Penalties, Adjudication Offences (Sections 45 to 47 & Sections 65 to 78). Cyber Regulations Appellate Tribunal (Sections 48 to 64).

Text and Reference Books:

Note: In each theory paper, nine questions are to be set. Two questions are to set from each Unit and candidate is required to attempt one question from each unit. Question number nine will be compulsory, which will be of short answer type with 5-10 parts, out of the entire syllabus. In all, five questions are to be attempted.

MT-E05 Cloud Computing

UNIT-I
Introduction to Cloud Computing, Definition, Characteristics, Components, Cloud provider, SAAS, PAAS, IAAS and Others, Organizational scenarios of clouds, Administering & Monitoring cloud services, benefits and limitations, Deploy application over cloud, Comparison among SAAS, PAAS, IAAS Cloud computing platforms: Infrastructure as service: Amazon EC2, Platform as Service: Google App Engine, Microsoft Azure, Utility Computing, Elastic Computing
UNIT-II

Service oriented analysis: Business-centric SOA - Deriving business services-service modeling - Service Oriented Design - WSDL basics - SOAP basics - SOA composition guidelines - Entity-centric business service design - Application service design - Task centric business service design.

UNIT-III

Cloud Technology: Introduction to Cloud Technologies, Study of Hypervisors Compare SOAP and REST Webservices, AJAX and mashups-Web services: SOAP and REST, SOAP versus REST, AJAX: asynchronous 'rich' interfaces, Mashups: user interface services
Virtualization Technology: Virtual machine technology, virtualization applications in enterprises, Pitfalls of virtualization
Multitenant software: Multi-entity support, Multi-schema approach, Multitenance using cloud data stores, Data access control for enterprise applications,
Data in the cloud: Relational databases, Cloud file systems: GFS and HDFS, BigTable, HBase and Dynamo.

UNIT-IV

Cloud security fundamentals, Vulnerability assessment tool for cloud, Privacy and Security in cloud

Text Books:

2. Enterprise Cloud Computing by Gautam Shroff,Cambridge
4. Cloud Security by Ronald Krutz and Russell Dean Vines, Wiley-India
Reference Books:

1. Google Apps by Scott Granneman, Pearson
2. Cloud Security & Privacy by Tim Malhar, S. Kumaraswammy, S. Latif (SPD, O'Reilly)
4. Cloud Computing Bible by Barrie Sosinsky, Wiley India
5. Stefano Ferretti et.al., QoS–aware Clouds”, 2010 IEEE 3rd International Conference on Cloud Computing

Note: In each theory paper, nine questions are to be set. Two questions are to be set from each Unit and candidate is required to attempt at least one question from each unit. Question number nine will be compulsory, which will be of short answer type with 5-10 parts, out of the entire syllabus. In all, five questions are to be attempted.

MCA – E06 Distributed Systems

UNIT – I
Introduction and Architectures: Definition of a Distributed System, Goals and Types of distributed systems, Architecture Styles, System Architectures, Middleware, Self-management in Distributed Systems with examples of Astrolabe, Globule and Jade.
Processes: Threads, Virtualization, Clients, Servers and Code Migration

UNIT – II
Communication: Remote Procedure Call, Message-Oriented, Stream Oriented and Multicast Communication
Naming: Names, Identifiers and Addresses, Flat naming, Structured Naming and Attribute-Based Naming.

UNIT – III
Synchronization: Clock Synchronization, Logical Clocks: Lamport’s Logical Clocks and Vector Clocks, General Introduction to the Concepts of Replication and Fault Tolerance
Distributed File Systems: Client-Server Architecture in NFS, Cluster-based Architecture in Google, Symmetric Architectures, RPC in NFS.

UNIT – IV
Distributed Web-Based Systems: Architecture, Processes i.e. clients, Apache Web Server and Web Server Clusters, Communication i.e. HTTP and Simple Object Access Protocol, Web Proxy Caching.
Case studies of Mach, Chorus and Amoeba distributed operating systems

Text Book:
Reference Books:

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MT – E07 Graph Theory

UNIT – I
Introduction – Definition of a graph, application of graphs, finite and infinite graphs, incidence and degree, isolated vertex, pendant graph, null graph. Path and circuits-Isomorphism, subgraphs, walks, paths, circuits, connected graphs, disconnected graphs and its components, Euler graph, operations on graphs, Hamiltonian paths and circuits, travelling salesman problem.

UNIT – II
Trees and fundamental circuits- Trees, properties of the trees, pendant vertices in a tree, distance and centres in a tree, rooted and binary trees, on counting trees, spanning tree, fundamental circuits, finding all spanning trees of a graph, spanning tree in a weighted graph.

UNIT – III
Planar and Dual graphs- combinatorial Vs. Geometric Graphs, planar graphs, different representations of a planar graph, detection of planarity, Geometric Dual, combinatorial dual, thickness and crossings, Matrix representation of graphs- Incidence graph, submatrices of A(G), circuit matrix, cut-set matrix, path matrix adjacency matrix.

UNIT – IV
Directed Graphs- Definition of a directed graph, types of digraphs, digraphs and binary relations, directed path and connectedness, trees with directed edges, fundamental circuits in a digraph, adjacency matrix of a graph, acyclic digraphs and decyclization.
Graph algorithms- algorithm for connectedness, a spanning tree, a set of fundamental circuits, directed circuits, shortest path algorithm, depth search first on a graph, algorithm for planarity testing, algorithm for isomorphism.

Text Book:
1. Narsingh Deo, “Graph Theory”, Prentice Hall of India.
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